

2009-12-01



## Professor Sheng-Lyang Jang

Ph.D., University of Florida, U.S.A.

Field of study:

- 1) Radio-Frequency Integrated Circuits and Systems
- 2) Semiconductor Devices
- 3) Photonics

Key words: RF, MOS

URL: <http://homepage.ntust.edu.tw/SLJJJ/>

Email: [sljjj@mail.ntust.edu.tw](mailto:sljjj@mail.ntust.edu.tw)

Phone: 886-2-27376383(voice), 886-2-27376424(Fax)

我們研究群(光半組 系統組 光電所)的研究內容整合了不同領域知識

Interdisciplinary knowledge includes:

Semiconductor process-Device-Integrated circuits-Photonics-Communication systems

畢業後容易在產業界找到好工作 繼續升學

不景氣時不容易被裁員

實驗室的研究成果比美美國一流大學研究所

畢業生有自信心 能獨立開發產品 與台大學生平起平坐

### 1. The Subject of Research

- (1) Design and implement analog front-end circuits using the CMOS & other

technologies.

1GHz-200GHz circuits for RF, analog system or mixed-signal integrated system application.

The circuits include power amplifier, low noise amplifier, mixer, voltage controlled oscillators, and injection locked frequency dividers.

Design and implementation of passive on-chip components: microstrip line, inductor, filter and antenna.

RF system design

(2) Nanometer semiconductor devices: modeling and characterization.

Spin-electronics, Nanometer silicon-based semiconductor devices: process and design, quantum tunneling current, hot-carrier stress. Design of polysilicon thin film transistor and its circuit for the LCD application

3) Photonics

III-V Compound Semiconductors. OptoElectronic Integrated Circuits (OEIC).

Optical and RF Electronic system design. Photodetector, solar cell, laser diode, LCD.

Integrated solar cell with IC.

4) MEMS and sensor.

## 2. Publications:

Journals:

### REFERENCES

- [1] M.-H. Juang, C. W. Chang, J. L. Wang, D. C. Shye, C. C. Hwang, S. L. Jang, " Formation of n-channel polycrystalline-Si thin-film transistors by dual source/drain implantation , " in press, Solid-State Electron.
- [2] M.-H. Juang, P.-S. Hu, S.L. Jang, " Formation of polycrystalline-Si thin-film transistors with tunneling field effecttransistor structure," in press, *Thin Solid Films*.
- [3] M.-H. Juang, C.W. Chang, C.W. Huang, J.L. Wang, D.C. Shye, C.C. Hwang, S.L. Jang, " Formation of sub-micrometer polycrystalline-SiGe thin-film transistors by using a thinned channel layer , " in press, Solid-State Electron.
- [4] M.-H. Juang, C.W. Huang, M.-L. Wu, C.C. Hwang, J.L. Wang, D.C. Shye, S.-L. Jang, " Formation of n-channel polycrystalline-Si thin-film transistors by using retrograde channel scheme with double implantation," in press, Microelectronic Engineering , 2009.
- [5] Sheng-Lyang Jang, Chuang-Jen Huang, Ching-Wen Hsue, and Chia-Wei Chang, " A 0.3V Cross-coupled VCO using Dynamic Threshold MOSFET," accepted, *IEEE Microw. Wireless Compon. Lett.*, 2009.

[2010]

- [6] Sheng-Lyang Jang, Cheng-Chen Liu, and Jhin-Fang Huang, " Divide-by-3 Injection-Locked Frequency Divider Using Two Linear Mixers," *IEICE Trans. on Electron.*, Vol.E93-C, No.1, pp.-, Jan. 2010.
- [7] Yuan-Kai Wu, Jhin-Fang Huang, Chia-Wei Chang and Sheng-Lyang Jang, " An 8-phase  $\div$  4 SiGe HBT Ring-Oscillator-Based Injection Locked Frequency Divider," *Microwave and Optical Technology Lett.*, vol. 52, No.1, pp. 201-204, 2010.

- [8] Cheng-Chen Liu, Sheng-Lyang Jang, Jhao-Jhang Chen, and Miin-Horng Juang, "A 0.6V Low Power Armstrong VCO in  $0.18\mu\text{m}$  CMOS," *Microwave and Optical Technology Lett.*, vol. 52, No.1, pp. 116-119, 2010.
- [9] Chuang-Jen Huang, Ching-Wen Hsue, Cheng-Chen Liu, and Sheng-Lyang Jang, "A 17GHz Colpitts VCO Using Reverse- and Forward-biased Diode Tuning in  $0.18\mu\text{m}$  CMOS," *Microwave and Optical Technology Lett.*, vol. 52, No.1, pp. 38-41, 2010.
- [2009]
- [10] Sheng-Lyang Jang, Chi-Wen Lin, Cheng-Chen Liu, and Miin-Horng Juang, "Tail-injected Divide-by-4 Quadrature Injection Locked Frequency Divider," *Int. J. Electronics*. Vol. 96, No. 12, pp.1225-1235, 2009.
- [11] Sheng-Lyang Jang, Tai-Sung Lee, Ching-Wen Hsue and Cheng-Chen Liu, "A Low Voltage Quadrature VCO Implemented with Series Frequency Doublers," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, No. 12, pp.819-821, Dec. 2009.
- [12] Sheng-Lyang Jang, Yuan-Kai Wu, Cheng-Chen Liu and Jhin-Fang Huang, "A Dual-Band CMOS Voltage-Controlled Oscillator Implemented with Dual-Resonance LC Tank," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, No. 12, pp.816-818, Dec. 2009.
- [13] Sheng-Lyang Jang, Tai-Sung Lee, Ching-Wen Hsue and Chia-Wei Chang, "A Low Voltage and Low Power Bottom-Series Coupled Quadrature VCO," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, No. 11, 722-724, Nov., 2009.
- [14] Sheng-Lyang Jang, Chia-Wei Chang and Sheng-Ming Yang, "Low Power Wide-Locking Range CMOS Quadrature Injection-Locked Frequency Divider," *Microwave and Optical Technology Lett.*, vol. 51, No.10, pp. 2420-2423, 2009.
- [15] Sheng-Lyang Jang, Cheng-Chen Liu, Jhin-Fang Huang, Yuan-Kai Wu, and Jhao-Jhang Chen, "Quadrature VCOs Using Single-Ended Injected Injection-Locked Frequency Dividers," *IEICE Trans. on Electron.*, Vol.E92-C, No.9, pp.1226-1229, Sept. 2009.
- [16] Sheng-Lyang Jang, Chuang-Jen Huang, Cheng-Chen Liu, and Ching-Wen Hsue, "A 0.22V Quadrature VCO in 90nm CMOS Process," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, No. 9, 566-568, Sept., 2009.
- [17] Miin-Horng Juang, C.W. Huang, C.W. Chang, D.C. Shye, C.C. Hwang, J.L. Wang, S.L. Jang, "The formation of polycrystalline-Si thin-film transistors by using large-angle-tilt-implantation of dopant through gate sidewall spacer," *Solid-State Electron.*, vol. 53, No. 9, pp. 1036-1040, Sept., 2009.
- [18] Sheng-Lyang Jang, Chien-Feng Lee, and Chia-Wei Chang , " A K-Band Differential Colpitts Cross-Coupled VCO in  $0.13\mu\text{m}$  CMOS," *Solid-State Electron.*, vol. 53, No. 9, pp. 931-934, Sept., 2009.
- [19] Sheng-Lyang Jang, Ren-Kai Yang, Cheng-Chen Liu, and Ching-Wen Hsue, "A Low Power SiGe BiCMOS Series-Tuned Divide-by-3 Injection Locked Oscillators , " *Microwave and Optical Technology Lett.*, vol. 51, No.9, pp. 2239-2242, 2009.
- [20] Sheng-Lyang Jang, Cheng-Chen Liu, Shin-Hsin Huang, and Miin-Horng Juang, "Quadrature Cross-Coupled VCO Implemented with Body Injection-locked Frequency Dividers , " *Microwave and Optical Technology Lett.*, vol. 51, No.8, pp. 1918-1921, 2009.
- [21] Sheng-Lyang Jang, Jyun-Yan Wun, Cheng-Chen Liu, and Miin-Horng Juang, "A Low Power LC-tank SiGe BiCMOS Injection Locked Frequency Divider," *Microwave and Optical Technology Lett.*, vol. 51, No.8, pp. 1970-1973, 2009.
- [22] S.-L. Jang, Chang-Hao Yang, Cheng-Chen Liu and M.-H. Juang, "A Wide-locking Range 6-Phase  $\div 3$  Injection Locked Frequency Divider," *Int. J. Electronics*. Vol. 96 , No. 7, pp. 691-697, July 2009.
- [23] Sheng-Lyang Jang, Yi-Jhe Song, and Cheng-Chen Liu, "A differential Clapp VCO in  $0.13\mu\text{m}$  CMOS Technology," *IEEE Microw. Wireless Compon. Lett.*, pp. 404-406, June, 2009.
- [24] Sheng-Lyang Jang, Kuan-Chun Shen, Chia-Wei Chang, and Miin-Horng Juang, "A 6-Phase  $\div 3$  injection locked frequency divider in SiGe BiCMOS technology," *Microwave and Optical Technology Lett.*, pp. 1555-1557, June, 2009.
- [25] Sheng-Lyang Jang, Ren-Kai Yang, Chia-Wei Chang and Miin-Horng Juang, "Multi-modulus LC injection-locked frequency dividers using single-ended injection," *IEEE Microw. Wireless Compon. Lett.*, pp. 311-313, May, 2009.
- [26] Sheng-Lyang Jang, Chien-Feng Lee and Jhong-Chen Luo, "A CMOS LC Injection-Locked Frequency Divider with the Division Ratio of 2 and 3," *Microwave and Optical Technology Lett.*, pp. 1263-1267, May 2009.
- [27] Sheng-Lyang Jang, Chia-Wei Chang, Ming-Hsiang Suchen and Kuan-Chun Shen, "A Differential VCO Using the Drain-Connected-to-Body MOSFET," *Microwave and Optical Technology Lett.*, pp. 1174-1177, May 2009.
- [28] Sheng-Lyang Jang, Cheng-Chen Liu, Ming-Hsiang Suchen, and Shih-Hsin Huang," An Eight-Phase CMOS Voltage Controlled Oscillator," *Microwave and Optical Technology Lett.*, pp. 1225-1228, May 2009.
- [29] Sheng-Lyang Jang, Chih-Yeh Lin, Cheng-Chen Liu, and Jhin-Fang Huang, " Dual-Band CMOS Injection-Locked Frequency Divider With Variable Division Ratio," *IEICE Trans. on Electron.*, Vol.E92-C, No.4, pp.550-557, Apr. 2009.
- [30] Sheng-Lyang Jang, Kuan-Chun Shen, and Cheng-Chen Liu," A 5.2GHz Low Power VCO in  $0.18\mu\text{m}$  CMOS Process," *Microwave and Optical Technology Lett.*, pp. 1052-1055, April 2009.

- [31] Sheng-Lyang Jang, Chun-Yi Wu, Cheng-Chen Liu, and Miin-Horng Juang, "A 5.6GHz Low Power Balanced VCO in 0.18μm CMOS," *IEEE Microw. Wireless Compon. Lett.*, pp. 233-235, April, 2009.
- [32] Sheng-Lyang Jang, Shin-Hsin Huang, Cheng-Chen Liu and Miin-Horng Juang, "CMOS Colpitts Quadrature VCO Using the Body Injection-Locked Coupling Technique," *IEEE Microw. Wireless Compon. Lett.*, pp. 230-232, April, 2009.
- [33] Sheng-Lyang Jang, Cheng Chen Liu and Chia-Wei Chung, "A Tail-injected Divide-by-4 SiGe HBT Injection Locked Frequency Divider," *IEEE Microw. Wireless Compon. Lett.*, pp. 236-238, April, 2009.
- [34] Miin-Horng Juang, S.-H. Cheng, and S.-L. Jang, "Formation of polycrystalline-Si thin-film-transistors with a retrograde channel doping profile," *Solid-State Electron.* 53, No. 3, pp. 371-375, 2009.
- [35] S.L. Jang, C.W. Chang, W.C. Cheng, C.F. Lee and M. H. Juang, "Low Power Divide-By-3 Injection-Locked Frequency Dividers Implemented with Injection Transformers," *IEE Electronics Lett.*, vol. 45, pp. 240-241, Feb. 2009.
- [36] Sheng-Lyang Jang, Jhong-Chen Luo, Chia-Wei Chang, Chien-Feng Lee and Jhin-Fang Huang, "LC-tank Colpitts Injection-Locked Frequency Divider with Even and Odd Modulo," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, no. 2, pp. 113-115, Feb. 2009.
- [37] Sheng-Lyang Jang, Cheng-Pin Liu, Chien-Feng Lee and Ching-Wen Hsue, "Quadrature and Eight-phase VCOs Implemented with SiGe Injection Locked Frequency Dividers," *Microwave and Optical Technology Lett.*, pp. 395-399, Feb. 2009.
- [38] M. H. Juang, P.-S. Hu, and H. C. Cheng, "Formation of lateral SiGe tunneling field-effect transistors on the SiGe/oxide/Si-substrate," *Semicond. Sci. Technol.* 24, No. 2, 025019, Feb., 2009.
- [39] Sheng-Lyang Jang, Chi-Wen Lin, Cheng Chen Liu, and M.-H. Juang, "An active-inductor injection locked frequency divider with variable division ratio," *IEEE Microw. Wireless Compon. Lett.*, vol. 19, no. 1, pp. 39-41, Jan. 2009.
- [40] Chien-Feng Lee, and Sheng-Lyang Jang, "A 24-GHz 90-nm CMOS injection-locked frequency divider," *Microwave and Optical Technology Lett.*, pp. 32-36, Jan. 2009.

[ 2008 ]

- [41] Sheng-Lyang Jang, Cheng-Chen Liu and Ching-Wen Hsue, "LC-Tank Injection Locked Frequency Divider with Variable Division Ratio," *Microwave and Optical Technology Lett.*, pp. 3232-3236, Dec. 2008.
- [42] Sheng-Lyang Jang and Cheng-Chen Liu, "Wide-Locking Range Divide-by-4 Injection-Locked Frequency Dividers," *Microwave and Optical Technology Lett.*, pp. 3229-3232, Dec. 2008.
- [43] Sheng-Lyang Jang, S.-S. Huang, Chien-Feng Lee, and M.-H. Juang, "CMOS Quadrature VCO implemented with two first-harmonic injection-locked oscillators," *IEEE Microw. Wireless Compon. Lett.*, pp.695-697, Oct. 2008.
- [44] Sheng-Lyang Jang, Sheng-Chien Wu, Chien-Feng Lee and M.-H. Juang, "CMOS top-series coupling quadrature injection-locked frequency divider," *Microwave and Optical Technology Lett.*, pp. 2554-2557, Oct. 2008.
- [45] Sheng-Lyang Jang, Pei-Xi Lu, Chien-Feng Lee and M.-H. Juang, "Divide-by-3 LC injection locked frequency divider with a transformer as an injector's load," *Microwave and Optical Technology Lett.*, pp. 2722-2725, Oct. 2008.
- [46] S.-L. Jang and C.-C. Liu, "Active-Inductor-Capacitor Tank Colpitts Injection Locked Frequency Divider," *Microwave and Optical Technology Lett.*, pp. 2376-2379, Sept, 2008.
- [47] S.-L. Jang, S.-S. Huang, J.-F. Lee and M.-H. Juang, "LC-tank Colpitts injection-locked frequency divider with record locking range," *IEEE Microw. Wireless Compon. Lett.*, pp.560-562, Aug. 2008.
- [48] Sheng-Lyang Jang, Chia-Wei Chang, Sheng-Chien Wu, Chien-Feng Lee, Lin-yen Tsai, and Jhin-Fang Huang, "Quadrature Hartley VCO and injection-locked frequency divider," *IEICE Trans. on Electron.*, Vol.E91-C, No.8, pp.1371-1374, Aug. 2008.
- [49] M. H. Juang, I.-S. Tsai, and H. C. Cheng, "The formation of polycrystalline-Si thin-film transistors with a thinned channel layer," *Semicond. Sci. Technol.* 23, No. 8, 105003, Aug., 2008.
- [50] M. H. Juang, I.-S. Tsai, S. L. Jang and H. C. Cheng, "Formation of thin-film transistors with a polycrystalline hetero-structure channel layer," *Semicond. Sci. Technol.* 23, No. 8, 085017, July, 2008.
- [51] Sheng-Lyang Jang, Chih-Yeh Lin, and Chien-Feng Lee, "A low voltage 0.35um CMOS frequency divider with the body injection technique," *IEEE Microw. Wireless Compon. Lett.*, vol. 18, no. 7, pp.470-472, July, 2008.
- [52] Chien-Feng Lee and Sheng-Lyang Jang, "A low voltage divide-by-3 injection-locked frequency divider," *Microwave and Optical Technology Lett.*, pp. 1905-1908, July, 2008.
- [53] Sheng-Lyang Jang, Chia-Wei Chang, Chien-Feng Lee, and Jhin-Fang Huang, "Divide-by-3 LC Injection Locked Frequency Divider Implemented with 3D Inductors," *IEICE Transaction on Electronics.*, Vol.E91-C, No.6,pp.956-962, Jun. 2008.
- [54] Sheng-Lyang Jang, Chia-Wei Tai, and Chien-Feng Lee, "Divide-by-3 injection locked frequency divider implemented with active inductor," *Microwave and Optical Technology Lett.*, Vol. 50, no. 6, pp.1682-1685, June, 2008.

- [55] Sheng-Lyang Jang, Ming-Hsiang Suchen, and Chien-Feng Lee, " Colpitts injection locked frequency divider implemented with a 3D helical transformer , " *IEEE Microw. Wireless Compon. Lett.*, vol. 18, no. 6, pp.410-412, June, 2008.
- [56] Sheng-Lyang Jang, S.-S. Huang, Sheng-Chien Wu, Chien-Feng Lee and M.-H. Juang , " A low power X-band CMOS differential VCO , " *Microwave and Optical Technology Lett.*, Vol. 50, no. 5, pp.1389-1392, May, 2008.
- [57] Chien-Feng Lee and Sheng-Lyang. Jang, " A novel divide-by-3 Hartley injection-locked frequency divider," *Microwave and Optical Technology Lett.*, Vol. 50, no. 4, pp.906-909, April. 2008.
- [58] Sheng-Lyang. Jang, Wei-Chi Chen, and Chien-Feng Lee, " Divide-by-3 LC injection locked frequency divider with inductor over MOS topology , " *Microwave and Optical Technology Lett.*, Vol. 50, no. 4, pp.988-992, April. 2008.
- [59] Sheng-Lyang Jang, Fei-Hung Chen, and J.-F. Huang, " A transformer-coupled LC-tank injection locked frequency divider," *Microwave and Optical Technology Lett.*, Vol. 50, no. 3, pp.592-595, Mar. 2008.
- [60] Sheng-Lyang Jang, and C.-C. Liu, " A varactorless CMOS direct-injection locked frequency divider , " *Microwave and Optical Technology Lett.*,Vol. 50, no. 3, pp.608-611, Mar. 2008.
- [61] Sheng-Lyang Jang, Fei-Hung Chen, Chien-Feng Lee, and M.-H. Juang, " An LC-tank injection locked frequency divider with record locking range percentage," *Microwave and Optical Technology Lett.*, Vol. 50, no. 3, pp.806-809, Mar. 2008.
- [62] S.-L. Jang, C.-C. Liu, and J.-F. Huang " A wide locking range quadrature injection locked frequency divider with tunable active inductor," *IEICE Transaction on Electronics.*, Vol.E91-C, No.3, pp.373-377, Mar. 2008.
- [63] Sheng-Lyang Jang, Chien-Feng Lee, and Wei-Hsung Yeh, " A divide-by-3 injection locked frequency divider with single-ended input," *IEEE Microw. Wireless Compon. Lett.*, pp. 142-144, Feb. 2008.
- [64] S.-L. Jang, Jui-Cheng Han, Chien-Feng Lee, and J.-F. Huang, " A small die area and wide locking range CMOS frequency divider , " *Microwave and Optical Technology Lett.*,Vol. 50, no. 2, pp.541-544, Feb. 2008.
- [65] M. H. Juang, C. L. Chen, and S. L. Jang, "Study of shallow trench isolation technology with a poly-Si sidewall buffer layer," *Semicond. Sci. Technol.* 23, no. 1, 015002, 2008.
- [66] Sheng-Lyang Jang, W. Yeh and Chien-Feng Lee, " A low power CMOS divide-by-3 LC-tank injection locked frequency divider , " *Microwave and Optical Technology Lett.*,Vol. 50, no. 1, pp.259-262, Jan. 2008.
- [67] Sheng-Lyang Jang, Lin-yen Tsai and Chien-Feng Lee, " A CMOS switched resonator frequency divider tuned by the switch gate bias , " *Microwave and Optical Technology Lett.*,Vol. 50, no. 1, pp.222-225, Jan. 2008.
- [68] Sheng-Lyang Jang, Y.-J. Wu and Chien-Feng Lee , " A 3.5GHz low power and phase noise differential CMOS VCO," *Microwave and Optical Technology Lett.*,Vol. 50, no. 1, pp.153-156, Jan. 2008.

[2007]

- [69] C.-F. Lee, S.-L. Jang, and M.-H. Juang, " A wide locking range differential Colpitts injection locked frequency divider," *IEEE Microw. Wireless Compon. Lett.*, Vol. 17, No. 11, pp. 790-792, Nov. 2007. (2003 SCI Impact factor : 1.457)
- [70] S.-L. Jang, Che Yi Lin, C.-F. Lee, and M.-H. Juang," A complementary Hartley injection-locked frequency divider," *Microwave and Optical Technology Lett.*, pp.2817-2820, Nov. 2007. (2003 SCI Impact factor : 0.5)
- [71] Sheng-Lyang Jang, Shao-Hua Lee, Chun-Yuan Chiu and Chien-Feng Lee, " A dual band CMOS complementary Colpitts voltage controlled oscillator," *Microwave and Optical Technology Lett.*, pp.2634-2637, Nov. 2007.
- [72] S.-L. Jang, Y.-J. Wu, C.-F. Lee and M.-H. Juang, " A Clapp LC-tank injection locked frequency divider," *Microwave and Optical Technology Lett.*, pp.2625-2628, Nov. 2007.
- [73] Sheng-Lyang Jang, Yun-Hsueh Chuang, Shao-Hwa Lee and Yeuh-Hua, Chiang," A current reused CMOS quadrature injection locked frequency divider , " *Microwave and Optical Technology Lett.*, pp.1804-1806, Aug. 2007.
- [74] Sheng-Lyang Jang, and Chien-Feng Lee , " A wide locking range LC-tank injection locked frequency divider," *IEEE Microw. Wireless Compon. Lett.*, pp.613-615, Aug., 2007.
- [75] Shao-hua Lee, Sheng-Lyang Jang, Chien-Feng Lee, and M.-H. Juang , " Wide locking range divide-by-4 injection locked frequency dividers," *Microwave and Optical Technology Lett.*, Vol. 49, No. 7, pp. 1533-1536, July 2007.
- [76] Yun-Hsueh Chung, Sheng-Lyang Jang, and Shao-Hua Lee, " A wide band injection locked frequency divider with variable inductor load," *IEEE Microw. Wireless Compon. Lett.*, pp.460-462, June, 2007.
- [77] Sheng-Lyang Jang, Shao-hua Lee, and Yen-Ruei Wu, " A CMOS LC-tank frequency divider with 3D helical inductors," *Microwave and Optical Technology Lett.* Vol. 49, No. 6, pp. 1477-1480, June 2007.

- [78] Shao-Hua Lee, Sheng-Lyang Jang, and Yun-Hsueh Chung, "A low voltage divide-by-4 injection locked frequency divider with quadrature outputs," *IEEE Microw. Wireless Compon. Lett.*, pp.373-375, May, 2007.
- [79] Sheng-Lyang Jang and C.-F. Lee, "A low voltage and power LC VCO implemented with dynamic threshold voltage MOSFETs," *IEEE Microw. Wireless Compon. Lett.*, pp.376-378, May, 2007.
- [80] S.-L. Jang, Y.-H. Chuang, S.-H. Lee, and J.-J. Chao, "Circuit techniques for CMOS divide-by-4 frequency divider," *IEEE Microw. Wireless Compon. Lett.*, pp.217-219, March 2007.
- [81] S.-H. Lee, S.-L. Jang, Y.-H. Chuang, J.-J. Chao, J.-F. Lee, and M.-H. Juang, "A low power injection locked LC-tank oscillator with current reused topology," *IEEE Microw. Wireless Compon. Lett.*, pp.220-222, March 2007.
- [82] Shao-Hwa Lee, Yun-Hsueh Chuang, Sheng-Lyang Jang, Ming-Tsung Chuang, and Ren-Hong Yen, "A quadrature CMOS VCO using transformer coupling and current reuse topology," *IEICE Trans. Commun.*, VOL. E90-B, pp.346-348, Feb. 2007.( 0.358)
- [83] Shao-Hua Lee, Yun-Hsueh Chuang, Sheng-Lyang Jang and Chien-Cheng Chen, "Low-phase noise Hartley differential CMOS voltage controlled oscillator," *IEEE Microw. Wireless Compon. Lett.*, pp. 145-147, Feb. 2007.
- [84] S.-L. Jang, Y.-H. Chung, S.-H. Lee, L.-R. Chi, and J.-F. Lee, "An integrated 5/2.5GHz direct-injection locked quadrature LC VCO," *IEEE Microw. Wireless Compon. Lett.*, pp.142-144, Feb. 2007.
- [85] Y.-H. Chuang, S.-L Jang, S.-H. Lee, R.-H. Yen and J.-J. Jhao, "5 GHz low power CMOS differential Armstrong VCOs with balanced current-reused topology , " *IEEE Microw. Wireless Compon. Lett.*, pp. 139-141, Feb. 2007.
- [86] S.-L. Jang, S.-H. Lee, C.-C.Chiu, and Y.-H. Chuang, "A 6 GHz low power differential VCO," *Microwave and Optical Technology Lett.*, pp.76-79, Jan. 2007.

## [ 2006 ]

- [87] Yun-Hsueh Chuang, Shao-Hwa Lee, Ren-Hong Yen, Sheng-Lyang Jang, and M.-H. Juang," A low-voltage quadrature CMOS VCO based on voltage-voltage feedback topology , " *IEEE Microw. Wireless Compon. Lett.*, pp.696-698, Dec. 2006.
- [88] Shao-Hua Lee, Sheng-Lyang Jang, Yun-Hsueh Chung, and Chung-Ching Chiu," A frequency divider implemented with a sub-harmonic mixer and a divide-by-2 divider , " *IEEE Microw. Wireless Compon. Lett.*, pp.699-701, Dec. 2006.
- [89] Y.-H. Chuang, S.-H. Lee, S.-L. Jang, J.-J. Chao and M.-H. Juang," A ring-oscillator-based wide locking range frequency divider," *IEEE Microw. Wireless Compon. Lett.*, vol. 16, no. 8, pp. 470-472, Aug. 2006.
- [90] Y.-H. Chuang, S.-H. Lee, R.-H. Yen, S.-L. Jang, J.-F. Lee and M.-H. Juang," A wide locking range and low voltage CMOS direct injection-locked frequency divider , " *IEEE Microw. Wireless Compon. Lett.*, vol. 16, no. 5, pp. 299-301, May 2006.
- [91] M. H. Juang, W. C. Chueh, and S. L. Jang, "The formation of trench-gate power MOSFETs with a SiGe channel region," *Semicond. Sci. Technol.* 21 (2006), 799-802.
- [92] M. H. Juang, T. Y. Lin, and S. L. Jang, "Formation of Mo gate electrode with adjustable work function on thin Ta<sub>2</sub>O<sub>5</sub> high-k dielectric films," *Solid-State Electronics*, vol. 50, 114-118, 2006.

## [ 2005 ]

- [93] Heng-Fa Teng, S.-L. Jang and M.H. Juang, " An analytical high frequency noise model for hot-carrier stressed MOSFETs," *Japan Journal Applied Physics*, Vol.44, No. 1A, pp.38-43, 2005.

## [ 2004 ]

- [94] M. H. Juang, W. T. Chen, C.I. Ou-Yang, S. L. Jang, M. J. Lin, H. C. Cheng, "Fabrication of trench-gate power MOSFETs by using a dual doped body region," *Solid-State Electronics*,47, 1079-1085, 2004.

## [ 2003 ]

- [95] Heng-Fa Teng, S.-L.Jang, and M.H. Juang " A unified model for high-frequency current noise of MOSFETs," *Solid-State Electronics*,47, 2043-2048, 2003.
- [96] Heng-Fa Teng and S.-L. Jang, " A non-local channel thermal noise for nMOSFET's," *Solid-State Electronics*, pp. 815-819, 2003.
- [97] Chorng-Jye Sheu and S.-L. Jang, " Modeling of electron gate current and post-stress drain current of p-type silicon-on-insulator MOSFETs," *Solid-State Electronics*, pp. 705-711, 2003.

## [ 2002 ]

- [98] M.H. Juang, C.I. Ou-Yang and S.-L. Jang, " A design consideration of channel doping profile for sub-0.12um partially depleted SOI n-MOSFET's," *Solid-State Electronics*, pp.1117-1121, 2002.

[99] S.-L. Jang, and Shao-Hua Li, " Gate-coupled and zener diode triggering silicon controlled rectifiers for electrostatic discharge protection circuits," Solid-State Electron. 263-267, 2002.

[2001]

[100]S.-L. Jang, L.-S. Lin, and S.-H. Li, " Temperature-dependent dynamic triggering characteristics of SCR-type ESD protection circuits," Solid-State Electron. 45, pp. 2005-2009, 2001.

[101]S.-L. Jang, L.-S. Lin, and S.-H. Li, " MOSFET triggering silicon controlled rectifiers for electrostatic discharge protection circuits," Solid-State Electron. 45, pp.1799-1803, 2001.

[102]S.-L. Jang, L.-S. Lin, S.-H. Li, and H.-M. Chen, " Dynamic triggering characteristics of SCR-type ESD protection circuits," Solid-State Electron. 45, pp.1091-1097, 2001.

[103]S.-L. Jang, S.-H. Li and L.-S. Lin, " SCR-type ESD protection circuits with variable holding voltage," Solid-State Electron. 45, pp.689-696, 2001.

[2000]

[104]S.-L. Jang and J.-K. Lin, " Temperature-dependence of steady-state characteristics of SCR-type ESD protection circuits," Solid-State Electron. 44, pp.2139-2146, 2000.

[105]C.-J. Sheu and S.-L. Jang, " A MOSFET gate current model with the direct tunneling mechanism," Solid-State Electron. 44, pp.1819-1824, 2000.

[106]C.-J. Sheu and S.-L. Jang, " A physics-based electron gate current model for fully depleted SOI MOSFET's," Solid-State Electron. 44, pp.1799-1806, 2000.

[107]S.-L. Jang, C.-J. Sheu, and C.-B. Twu " A compact drain-current model for stacked-gate flash memory cells," Solid-State Electron. 44, pp.1447-1453, 2000.

[108]S.-L. Jang and C.-J. Sheu, " A nonlocal gate current and oxide trapping charge generation model for LDD and single-drain nMOSFETs," Solid-State Electron. 44, pp.1305-1314, 2000.

[109]S.-L. Jang, M.-S. Gau, and C.-K. Lin, " Novel diode-chain triggering SCR circuits for ESD protection," Solid-State Electron. 44, pp.1297-1303, 2000.

[110]C.-G. Chyau, S.-L. Jang and C.-J. Sheu, " A physics-based short-channel SOI MOSFET model for fully-depleted single drain and LDD devices," Solid-State Electron. 44, pp.487-499, 2000.

[1999]

[111]S.-L. Jang and H.-H. Lin, " Modeling of drain leakage current in SOI pMOSFETs," Solid-State Electron. 43, pp.2147-2154, 1999.

[112]S.-L. Jang, B.-R. Huang, and J.-J. Ju, " A unified analytical fully-depleted and partially-depleted SOI MOSFET model," IEEE Trans. Electron Devices, Sept., pp.1872-1876, 1999.

[113]C.-G. Chyau and S.-L. Jang, " A physics-based short-channel current-voltage model for buried-channel MOSFETs," Solid-State Electron. 43, pp.1177-1188, 1999.

[114]S.-L. Jang, C.-G. Chyau, and C.-J. Sheu, " Complete deep-submicron metal-oxide-semiconductor field-effect-transistor drain current model including quantum mechanical effects," Jpn. J. Appl. Phys. Part. 1, vol. 38, No. 2A, pp.687-688, 1999.

[115]S.-L. Jang and S.-S. Liu, " A novel approach for modeling accumulation-mode SOI MOSFETs," Solid-State Electron. 43, pp.87-96, 1999.

[116]Hong-Kee Jiou , Sheng-Lyang Jang and Shau-Shen Liu, " An analytical symmetric double-gate SOI MOSFET model , " Int. J. Electronics. Vol. 86, No. 6, pp.671-683, 1999.

[1998]

[117]S.-L. Jang, S.-S. Liu and C.-J. Sheu, " A compact LDD MOSFET I-V model based on nonpinned surface potential," IEEE Trans. Electron Devices, Vol. 45, No. 12, pp. 2489-2498, 1998.

[118]C.-G. Chyau and S.-L. Jang, " A compact pre- and post-stress I-V model for submicrometer buried-channel pMOSFET's," IEEE Trans. Electron Devices, Vol. 45, No. 10, pp. 2167-2178, 1998.

[119]S.-S. Liu, S.-L. Jang, and C.-G. Chyau, " Compact LDD nMOSFET degradation model," IEEE Trans. Electron Devices, Vol. 45, No. 7, pp.1538-1547, 1998.

[120]S.-L. Jang and S.-S. Liu, "New submicron and deep-submicron metal-oxide-semiconductor field-effect-transistor I-V and C-V model," Jpn. J. Appl. Phys. No. 6A, pp. 3942-3947, 1998.

[121]S.-L. Jang, H.-K. Chen and M.-C. Hu, " Low-frequency 1/f noise model for short-channel LDD MOSFETs," Solid-State Electron. No. 6, pp.891-899, 1998.

[122]S.-S. Liu, S.-L. Jang and C.-G. Chyau, "A new post-stress drain current model for surface-channel p-type metal-oxide-semiconductor field-effect-transistors," Jpn. J. Appl. Phys. No. 5A, pp. 2439-2444, 1998.

- [123] S.-L. Jang and S.-S. Liu, "An analytical surrounding gate MOSFET model," *Solid-State Electron.* vol. 42, no. 5, pp. 721-726, 1998.
- [124] S.-L. Jang, C.-G. Chyau, S.-S. Liu, and C.-M. Chiu, "A compact buried-channel lightly-doped-drain metal-oxide-semiconductor field-effect-transistor model," *Jpn. J. Appl. Phys. Part. 1*, No. 4A, pp. 1772-1780, 1998.
- [125] M.-C. Hu and S.-L. Jang, "An analytical fully-depleted SOI MOSFET model considering the effects of self-heating and source/drain resistance," *IEEE Trans. Electron Devices*, No. 4, p. 797-801, 1998.
- [126] M.-C. Hu and S.-L. Jang, "Deep-submicrometer fully-depleted SOI MOSFET drain current model for digital/analog circuit simulation," *Int. J. Electronics*. Vol. 84, No. 3, pp. 167-185, 1998.
- [127] S.-L. Jang, H.-K. Chen, and K.-M. Chang, "Low-frequency noise characteristics of hot carrier-stressed buried-channel pMOSFETs," *Solid-State Electron.* vol. 42, no. 3, pp. 411-418, 1998.
- [128] Y.-S. Chen and S.-L. Jang, "Modeling the asymmetric drain currents of hot-carrier stressed pMOSFETs operated in forward- and reverse-mode," *Solid-State Electron.* vol. 42, pp. 35-41, 1998.
- [129] S.-S. Liu and S.-L. Jang, "Deep-submicron lightly-doped-drain and single-drain metal-oxide-semiconductor transistor drain current model for analog and digital circuit simulation," *Jpn. J. Appl. Phys.* Vol. 37, No. 1, 64, 1998.

[1997]

- [130] S.-L. Jang and M.-C. Hu, "An analytical drain current model for submicrometer and deep submicrometer MOSFET's," *IEEE Trans. Electron Devices*, vol. 44, no. 11, pp. 1896-1902, 1997.
- [131] S.-L. Jang, M.-C. Hu and S.-S. Liu, "An analytical symmetric double-gate SOI MOSFET Model," *Jpn. J. Appl. Phys.* vol. 36, No. 10, pp. 6250-6253, 1997.
- [132] M.-C. Hu and S.-L. Jang, "A complete substrate current model for submicron and deep submicron MOSFETs," *Int. J. Electronics*. Vol. 83, pp. 159-176, 1997.
- [133] M.-C. Hu, S.-L. Jang, and C.-G. Chyau, "A physical short-channel current-voltage model for LDD MOSFETs" *Jpn. J. Appl. Phys.* vol. 36, Pt.1, No. 6A, pp. 3448-3459, 1997.
- [134] M.-C. Hu, S.-L. Jang, Y.-S. Chen, S.-S. Liu, and J.-M. Lin, "An analytical fully-depleted SOI MOSFET model considering the effects of self-heating, source/drain resistance, impact ionization and parasitic BJT," *Jpn. J. Appl. Phys.* pp. 2606-2613, 1997.
- [135] S.-L. Jang, M.-C. Hu, S.-S. Liu, and Y.-S. Chen, "A simple, analytical, and complete deep-submicrometer fully-depleted SOI MOSFET model considering velocity overshoot," *Jpn. J. Appl. Phys.* vol. 36, p. 1015, 1997.
- [136] S.-S. Liu, M.-C. Hu, and S.-L. Jang, "An analytical physics-based linear current-voltage model for hot-carrier damaged LDD nMOSFETs," *Solid-State Electron.* Vol. 41, No. 5, pp. 973-979, 1997.

[1996]

- [137] S.-L. Jang, T.-H. Tang, Y.-S. Chen and C.-J. Sheu, "Modeling of hot-carrier stressed characteristics of submicrometer p-MOSFETs," *Solid-State Electron.* No. 7, pp. 1043-1049, 1996.
- [138] S.-L. Jang, W.-M. Chen, H.-H Lin and C. H. Huang, "Low-frequency noise characteristics of AlInAs/InGaAs heterojunction bipolar transistors," *Solid-State Electron.* Vol. 39, No. 11, pp. 1581-1592, 1996.
- [139] Y.-S. Chen, Tz-Hua Tang and S.-L. Jang, "Modeling of hot-carrier-stressed characteristics of nMOSFETs," *Solid-State Electron.* Vol. 39, No. 1, pp. 75-81, 1996.

[1995]

- [140] S.-L. Jang, K. -Y. Chang, and J. K. Hsu, "Evidence of optical generation-recombination noise," *Solid-State Electron.* Vol. 38, No. 8, pp. 1449-1453, 1995.
- [141] S.-L. Jang, S.-S. Liu, and C.-J. Tsai, "Dynamic high-current stressing damage and post-stress relaxation in p-n-p silicon bipolar junction transistors," *Solid-State Electron.* Vol. 38, pp. 1387-1393, 1995.
- [142] S.-L. Jang, M.-C. Hu and Y.-S. Chen, "Current-voltage model of short-channel MOSFETs operated in the linear region," *Solid-State Electron.* No. 6, pp. 1239-1245, 1995.

[1994]

- [143] S.-L. Jang, "Analytical analysis of collector-base capacitance and cut-off frequency of  $n^+ - p^- - n^-$  bipolar junction transistors," *Solid-State Electron.* Vol. 37, pp. 15141-1545, 1994.

- [144] S.-L. Jang, and P.-C. Chang, "Degradation of npn bipolar junction transistors under dynamic high current stress," Solid-State Electron. Vol. 37, pp.295-301, 1994.
- [145] P.-C. Chang, S.-L. Jang, and Y.-S. Chen, "Degradation of bipolar junction transistors under dynamic high current stress and biased in open collector condition," Solid-State Electron. Vol. 37, pp.303-319, 1994.

[1993]

- [146] S.-L. Jang, "Formulation of mobility fluctuation 1/f noise in bipolar junction transistors," Solid State Electron. Vol. 36, pp.1541-1545, 1993.
- [147] S.-L. Jang, and P.-C. Chang, "Low -frequency noise characteristics of lightly doped drain MOSFETs," Solid State Electron. Vol. 36, pp.727-734, 1993.
- [148] S.-L. Jang, and F.-C. Liu, "Temperature dependence of the base current reversal and breakdown characteristics in n-p-n transistors," Solid State Electron. Vol. 36, pp. 617-621, 1993.
- [149] S.-L. Jang, "Current-voltage characteristics of Si bipolar junction transistors operated in the cutoff mode," Solid State Electron. Vol. 36, pp.291-292, 1993.
- [150] S.-L. Jang, "On the common-emitter breakdown voltage of bipolar junction transistors," Solid State Electron. Vol. 36, pp.213-216, 1993.
- [151] S.-L. Jang, and J.-Y. Wu, "Low-frequency current and intensity noise in AlGaAs laser diodes," Solid State Electron. Vol. 36, pp.189-196, 1993.

[1992]

- [152] S.-L. Jang, "Analytical Low-frequency 1/f noise model for lightly doped drain MOSFETs," Solid State Electron. Vol. 36, pp.1007-1010, 1992.
- [153] S.-L. Jang, Y.-S. Chen and P.-C. Chang, "Optical effects on the current voltage characteristics of lightly doped drain MOSFETs," Solid State Electron. Vol. 36, pp.727- 734, 1992.
- [154] S.-L. Jang, and F.-C. Liu and J.-Y. Wu, "Current reversals in p-n-p transistors," Solid State Electron. Vol. 35, pp.1781-1793, 1992.
- [155] S.-L. Jang, and K.-L. Chern, "Breakdown characteristics of emitter-base and collector-base junctions of silicon bipolar junction transistors," Solid State Electron. Vol. 35, pp.462-468, 1992.

[1991]

- [156] S.-L. Jang, and K.-L. Chern, "Hot-carrier-induced photovoltage in silicon bipolar junction transistors," Solid State Electron. Vol. 34, pp.1387-1392, 1991.
- [157] S.-L. Jang, "Effect of avalanche-induced light emission on the multiplication factor in bipolar junction transistors," Solid State Electron. Vol. 34, pp.1191-1196, 1991.
- [158] S.-L. Jang, "On the theory for the surface photovoltage technique based on the flat quasi-Fermi level approximation," Solid State Electron. Vol. 34, pp.373-377, 1991.

[1990]

- [159] S.-L. Jang, "A model of 1/f noise in polysilicon resistor," Solid State Electron. Vol. 33, pp.1155-1162, 1990.
- [160] S.-L. Jang, and G. Bosman, "The effect of field-dependent emission on the current-voltage characteristics of a  $p^+ - p^- - p^+$  Si:Au:B device," IEEE Trans. Electron Devices, Vol. 37, No.1, pp.222-226, 1990.

[1989]

- [161] S.-L. Jang, and G. Bosman, "Experimental evidence for a second-donor level of gold in silicon," J. Appl. Phys. 65- (12), pp.4809-4813, 1989.
- [162] S.-L. Jang, and G. Bosman, "Low field investigation of the gold donor level in silicon by noise and resistance measurements," J. Appl. Phys. 65(1), pp.201-204, 1989.

## Conferences

- [163] Sheng-Lyang Jang, Chia-Wei Chang, Yi-Jhe Song, Chun-Wei Hsu, and Cheng-Chen Liu, " On the Injection Methods in a Top Series-Injection Locked Frequency Divider," 2009 IEDMS.
- [164] Sheng-Lyang Jang, Chia-Wei Chang, Yi-Jhe Song, Yu-Sheng Chen, and Cheng-Chen Liu, " Low Power 0.35  $\mu$ m CMOS Divide-by-3 Injection-Locked Frequency Dividers," 2009 IEDMS.
- [165] Sheng-Lyang Jang, Cheng-Chen Liu, Ren-Kai Yang, Chih-Chieh Shih, and Chia-Wei Chang " A 0.35 $\mu$ m CMOS Divide-by-2 Quadrature Injection-Locked Frequency Divider," 2009 IEDMS.

- [166] Sheng-Lyang Jang, Cheng-Chen Liu, Jhao-Jhang Chen, Han-Sheng Chen, and Chia-Wei Chang " High Oscillation Frequency Active-Inductor Injection Locked Frequency Divider in 0.13 $\mu$ m CMOS," 2009 IEDMS.
- [167]Sheng-Lyang Jang, Cheng-Chen Liu, Yi-Jhe Song, and Miin Horng Juang, " An LC-tank Colpitts Injection-Locked Frequency Divider at Low Drain-Source Bias," 2009 the 20<sup>th</sup> VLSI Design/CAD Symposium.
- [168]Sheng-Lyang Jang, Ren-Kai Yang, Cheng-Chen Liu, Hsiu-An Yeh, and Ching-Wen Hsue, " Dual-Band Colpitts Injection-Locked Frequency Divider Using the Feedback Switching," 2009 the 20<sup>th</sup> VLSI Design/CAD Symposium.
- [169]Sheng-Lyang Jang, Chuang-Jen Huang, Cheng-Chen Liu, and Ching-Wen Hsue, Ying-Hsiang Liao, " A Differential VCO Using Two Complementary Cross-Coupled VCOs in 0.18um CMOS," 2009 the 20<sup>th</sup> VLSI Design/CAD Symposium.
- [170]Sheng-Lyang Jang, Cheng-Chen Liu and Chia-Wei Tai, " Implementation of 6-port 3D transformer in injection-locked frequency divider," IEEE Int. VLSI- DAT, 2009.
- [171]Sheng-Lyang Jang, Chuang-Jen Huang, and Cheng-Chen Liu , " A 0.35 $\mu$ m CMOS divide-by-3 LC injection-locked frequency divider," IEEE Int. VLSI- DAT, 2009.
- [172]Sheng-Lyang Jang, Che Yi Lin, and Chien-Feng Lee, " A 0.35um CMOS switched-inductor dual-band LC-tank frequency divider," IEEE Int. VLSI- DAT, pp. 240-242, 2008.
- [173]Sheng-Lyang Jang, Chun-Yuan Chiu, and Chien-Feng Lee , " A complementary Colpitts VCO implemented with ring inductor," IEEE Int. VLSI- DAT, 2008.
- [174]Hwan-Mei Chen, Chin-Chun Lin, Jia-Cing Lin, Sheng-Lyang Jang, "A double-looped complementary -Gm VCO," IEEE, int. conf. electron devices and solid-state circuits, 2007. Page(s):1009 – 1011.
- [175]Sheng-Lyang Jang, Hwan-Mei Chen, Jui-Cheng Han and Chien-Feng Lee, You-Da Jhuang, "A 5GHz low phase noise Hartley quadrature CMOS VCO,"IEEE, int. conf. electron devices and solid-state circuits, 2007.
- [176]H.-M. Chen , C.-C. Lin, J.-C. Lin and Sheng-Lyang Jang, " A 5.2GHz QVCO with bottom-series coupling coupling and switch transistor tail current," 2007 IEDMS.
- [177]Cheng Chen Liu , Che-Yi Lin, Chien-Feng Lee and Sheng-Lyang Jang, " A dual LC tanks CMOS VCO," 2007 IEDMS.
- [178]Cheng-Chen Liu, Chien-Feng Lee and Sheng-Lyang Jang, " An ultra low voltage CMOS injection locked frequency divider," 2007 IEDMS.
- [179]Yun-Hsueh Chuang, Shao-Hua Lee, Chien-Feng Lee, Sheng-Lyang Jang , and Min-Horng Juang. " A new CMOS VCO topology with capacitive degeneration and transformer feedback," IEEE Int. VLSI- DAT, pp.216-219, 2007.
- [180]Yun-Hsueh Chuang, Sheng-Lyang Jang, Shao-Hua Lee and Chien-Feng Lee, " low phase noise differential CMOS VCO based on tapped-inductor resonator," IEEE Int. VLSI- DAT, pp.220-223, 2007.
- [181]S.-L. Jang, Y.-H. Chuang, C.-C. Chen, J.-F. Lee, and S.-H. Lee , " A CMOS dual-band voltage controlled oscillator ,," 2006 IEEE APCCAS, D2-AM1-RM2.3 Dec., Singapore.
- [182]S.-H. Lee, Y.-H. Chuang, L.-R. Chi, S.-L. Jang, and J.-F. Lee , " A Low-Voltage 2.4GHz VCO with 3D Helical Inductors ,," 2006 IEEE APCCAS, D2-AM1-RM2.4 Dec., Singapore.
- [183]H.-M Chen, S.-H. Lee, and S.-L. Jang, " A double-feedback voltage controlled oscillators ,," Int. Conf. Solid state devices and materials, pp. 596-597, Yokohama, Japan, 2006.
- [184]S.-H. Lee, C.-C. Chiu, Y.-H. Chuang, S.-L. Jang, and J. -F. Lee, " A 5.2GHz Low Voltage and Low Power Differential Colpitts VCO ,," 2006 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'06), pp.33-36, Macao, P.R.C..
- [185] S.-L. Jang, Y.-H. Chuang, R.-H. Yen, and S.-H. Lee, "A 1.4GHz CMOS extremely-low voltage transformer-feedback VCO," 2006 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'06), pp.25-28, Macao, P.R.C..
- [186] S.-H. Lee , Y.-H. Chuang, Y-H Chiang, S.-L. Jang, and J.-F. Lee, " A 5GHz CMOS LC-VCO Using New Differentially-Tuned Varactor," 2006 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'06), pp.37-40, Macao, P.R.C..
- [187]Yun-Hsueh Chuang, Shao-Hua Lee, Chien-Feng Lee, Sheng-Lyang Jang , and Min-Horng Juang. " A new CMOS VCO topology with capacitive degeneration and transformer feedback," pp.33-36, 2006 Int. VLSI- DAT.
- [188]Sheng-Lyang Jang, Yun-Hsueh Chuang, Chien-Feng Lee and Shao-Hua Lee, " A 4.8GHz low-phase noise quadrature Colpitts VCO," pp.281-284, 2006 Int. VLSI- DAT.
- [189]S.-L. Jang, Y.-H. Chuang, Y. C. Wang,J. -F. Lee and S.-H. Lee, " Design of a dual-band LC-tank voltage controlled oscillator with the current reuse technique," Third conference on communication application, March 2005.Taiwan,pp.43-45
- [190]W.-C. Huang, Y.-H. Chuang, S.L. Jang, J. -F. Lee and S.-H. Lee, " Improvement of LC-VCO phase noise by layout optimization," Third conference on communication application, March 2005.Taiwan,pp.32-36
- [191]Sheng-Lyang Jang, Shao-Hua Li and Syue-Ming Lu, " Latchup Immune SCR Devices in CMOS Technology," APEMC, p.426-p.431, Dec. 2005.Taiwan.
- [192]S.-L. Jang, R.-H. Yen, Y.-H. Chuang, , J.-F. Lee, and S.-H. Lee," A low voltage 0.55V CMOS voltage controlled oscillator with transformer feedback" 2005 International Symposium on Communications (ISCOM2005).
- [193]Sheng-Lyang Jang, Chih-Ting Hu and Yun-Hsieh Chuang, " A New Current Source Temperature Compensation Circuit for Ring VCO," 2005 International Symposium on Communications (ISCOM2005).
- [194]S.-L. Jang, Y.-H. Chuang, Y.-C. Wang, J.-F. Lee, and S.-H. Lee, "A low power and low phase noise complementary colpitts quadrature VCO" 2005 International Symposium on Communications (ISCOM2005).
- [195]S.-L. Jang, C.-C. Lin, S.-H. Lee, Y.-H. Chuang and C.-F. Lee, " The design of Multi-layer transformer coupling oscillator," 2005 International Symposium on Communications (ISCOM2005)-00174.
- [196]S.-L. Jang, C.-C. Lin, S.-H. Lee, Y.-H. Chuang and C.-F. Lee, " A technique to reduce the turn-on time of VCO by the transient body-bias," 2005 International Symposium on Communications (ISCOM2005).
- [197]S.-L. Jang, Y.-H. Chuang, Y.-C. Wang, J.-F. Lee, and S.-H. Lee," Design of a dual-band LC-tank voltage controlled oscillator with the current reuse technique" 2005. ISMOT-142, 10th International Symposium on Microwave and Optical Technology (ISMOT 2005) August 22-25, 2005 Fukuoka, Japan

- [198]Y.-H. Chuang, S.-L. Jang, W.-C. Huang, S.-H. Lee and M.-H. Chuang," A wide-band fully-integrated CMOS oscillator tuned by voltage controlled transformer," 2005 ISMOT-159, August 22-25, 2005 Fukuoka, Japan
- [199]Y.-H. Chuang, J.-W. Hsu, S.-H. Lee, and S.-L. Jang, "A wide band fully-integrated CMOS oscillator tuned by switched transformer," 2005 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'05), pp.E2-9-E2-11, Beijing, P.R.C..
- [200]S.-L. Jang, C.-C. Lin, S.-H. Lee, Y.-H. Chuang, and C.-F. Lee, " Design of 1.8-GHz low Voltage controlled oscillators using the negative differential resistance concept," 2005 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'05), pp.E2-9-E2-11, Beijing, P.R.C..
- [201]Y.-H. Chuang, S.-L. Jang, W.-C. Huang, S.-H. Lee and M.-H. Chuang, " A wide-band fully-integrated CMOS oscillator tuned by voltage controlled transformer," 1<sup>st</sup> applied science and technology conference(ASTC)-photonics and communications, B02, 2004, Kaohsiung, Taiwan.
- [202]Heng-Fa Teng and S. -L. Jang "An analytical high frequency noise model for hot-carrier stressed MOSFETs," 7<sup>th</sup> International Conference on Solid-State and integrated Circuits Technology Proceedings, pp. 1135-1138, Oct. Beijing, China. (2004)
- [203]Shao-Hua Lee, S.-L. Jang, Yun-Hsueh Chuang and Jian-Feng Li, " A new LC-tank voltage controlled oscillator," 2004 IEEE APCCS, pp. 425 – 427.
- [204]Yun-Hsueh Chuang, S.-L. Jang, Jian-Feng Li and Shao-Hua Lee, " A low voltage 900MHz voltage controlled ring oscillator with wide tuning range," 2004 IEEE APCCS, P.1.26, Taiwan R.O.C..
- [205]Shao-Hua Lee, S.-L. Jang, Yun-Hsueh Chuang and Jian-Feng Li, " A 2.4GHz LC voltage controlled oscillator," 2004 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'04), pp.E2-9-E2-11, Taiwan R.O.C..
- [206]Yun-Hsueh Chuang, S.-L. Jang, Jian-Feng Li and Shao-Hua Lee, " A low voltage 900MHz voltage controlled ring oscillator with wide tuning range," 2004 Cross Strait Tri-regional Radio Science and Wireless Technology Conference (CSTRWC'04), pp.E2-1-E2-4, Taiwan R.O.C..
- [207]Heng-Fa Teng and S.-L. Jang, " A high-frequency noise model for SOI MOSFETs with thin silicon film," EDMS,pp. 826-829, Keelung Taiwan R.O.C., 2003.
- [208]Heng-Fa Teng and S.-L. Jang, " A non-local channel thermal noise for nMOSFET's," IEDMS,257-260, Taipei, Taiwan R.O.C., 2002.
- [209]S.-L. Jang, J.-Y. Wu, and F.-C. Liu, " Electrical 1/f noise in AlGaAsP/GaInP visible laer diodes," Proceedings of electron devices and material symposium, p.464, 1991, Taiwan, R.O.C..
- [210]K. -L. Chern, J. F. Huang and S.-L. Jang, " A study of two-layer lumped inductor," Proceedings of International symposium on communication, pp. 1-4, 1991, Taiwan, R.O.C..
- [211]S.-L. Jang and S.-S. Liu," A complete C-V model for submicrometer and deep submicrometer MOSFETs," EDMS, pp. 429-432, 1997, Chung-Li, Taiwan, R.O.C..
- [212]Y.-S. Chen and S.-L. Jang, " A complete asymmetric drain current model for post-stress submicron pMOSFET's," Int. Symp. VLSI Technology, Systems, and Application, pp.250-254, 1997.

### US Patents:

- [213]James Liu, Jimmy Hsieh, Sheng-Lyang Jang, and Hsueh-Ming Lu, "Latch-up-free ESD protection circuit using SCR," US patent# 7102864, Date Issued: September 5, 2006.
- [214]Sheng-Lyang Jang, and Shao-Hua Lee," Dual-band voltage controlled oscillator utilizing switched feedback technology", US patent, #7227425. Issued on June 5, 2007.
- [215]Sheng Lyang Jang, and Yun Hsueh Chuang," Low power consumption frequency divider circuit", US patent #7446617. Date Issued: November 4, 2008.
- [216]Sheng Lyang Jang, Yun Hsueh Chuang, and Shao-Hua Lee," Injection locked frequency divider", US patent #7522007. Date Issued: April 21, 2009.
- [217]Sheng Lyang Jang, Chun-Chieh Chao, Yun Hsueh Chuang, and Shao-Hua Lee," Injection locked frequency divider", US patent #7522008. Date Issued: April 21, 2009.
- [218]Sheng Lyang Jang, Shao-Hua Lee, Yun Hsueh Chuang, and Chung-Ching Chiu, " Back-gate coupling voltage control oscillato", US patent #7545230. Date Issued: June 9, 2009.
- [219]Sheng Lyang Jang, Yun Hsueh Chuang, Ren-Hong Yen and Shao-Hua Lee, " Multi-phase voltage-control oscillator", US patent #7551038. Date Issued: June 23, 2009.
- [220]Sheng Lyang Jang, Yun Hsueh Chuang, Ren-Hong Yen and Shao-Hua Lee, " Injection-locked frequency divider", US patent #07557668. Date Issued: July 7, 2009.
- [221]Sheng Lyang Jang, and Shao-Hua Lee, " Dual-band voltage controlled oscillator", US patent # 7589598. Date Issued: September 15, 2009.

### Taiwan Patents:

- [222]劉碩彰,謝志明, 張勝良, 呂學銘, 靜電放電保護電路, Taiwan patent number is I221668.
- [223]張勝良, 莊昀學,雙共振腔架構雙頻帶 LC 槽壓控振盪器電路. Sheng-Lyang Jang, Yuanhsueh Chuang, The two stacked LC-tank dual band voltage controlled oscillator, Taiwan patent number is I261962. issued date:11, Sep., 2006
- [224]張勝良,李少華, 利用切換回授路徑技術的雙頻帶壓控振盪器" A dual-band voltage controlled oscillator utilizing switched feedback technology", 2008, Taiwan patent number is I298579.

### Dissertations and books:

[225][Sheng Lyang Jang](#), MS thesis.

[226][Sheng Lyang Jang](#), *Trap Parameter Extraction of Deep Defects in Semiconductors Using Noise Measurements*. UMI Company, 1989, PhD. Thesis